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NEW QUESTION: 1If the processor is in User mode and then an IRQ interrupt occurs:**A.** CPSR mode bits are set to IRQ and SPSR_Irq mode bits are set to User.**B.** CPSR mode bits are set to User mode and SPSR_User mode bits are set to IRQ.**C.** CPSR mode bits are set to IRQ and SPSR_Irq mode bits are set to IRQ.**D.** CPSR mode bits are set to User and SPSR User mode bits are set to IRQ.**Answer: A**

NEW QUESTION: 2According to ISC2, what should be the fire rating for the internal walls of an information processing facility?**A.** All walls must have a two-hour minimum fire rating, except for walls to adjacent rooms where records such as paper and media are stored, which should have a three-hour minimum fire rating.**B.** All walls must have a two-hour minimum fire rating.**C.** All walls must have a one-hour minimum fire rating.**D.** All internal walls must have a one-hour minimum fire rating, except for walls to adjacent rooms where records such as paper and media are stored, which should have a two-hour minimum fire rating.**Answer: D****Explanation:** The internal walls of your processing facility must be a floor to ceiling slab with a one-hour minimum fire rating.Any adjacent walls where records such as paper, media, etc. must have a two-hour minimum fire rating.There are different regulations that exists for external walls from state to state.This topic is to illustrate that proper thickness of wall really helps in case of fire. This was demonstrated with some of the large bush fire that took place in California, we could see a few homes that were still standing because they were made of cement and fire resistant material.The ASTM (American Society for Testing and Meterials) is the organization that performs testing of material and set standards in this specific area.Source: Chris Hare's CISSP Study Notes on Physical Security, based on ISC2 CBK document.Source: CISSP Certification Exam Study Guide - All you need to pass the examAuthor: K. WanISBN: 9889732319The CISSP and CAP Prep GuideBy Ronald L. Krutz, Russell Dean Vines

NEW QUESTION: 3What happens when you attempt to compile and run the following code?
#include <vector>#include <iostream>#include <algorithm>using namespace std;class B { int val;public:B(int v):val(v){}int getV() const {return val;} bool operator < (const B & v) const { return val<v.val; } }; ostream & operator <<(ostream & out, const B & v) { out<<v.getV(); return out;} template<class T>struct Out { ostream & out; Out(ostream & o): out(o){} void operator() (const T & val) { out<<val<<" "; } }; int main() { B t1[]={3,2,4,1,5}; B t2[]={6,10,8,7,9}; vector v1(10,0); sort(t1, t1+5); sort(t2, t2+5); copy(t1,t1+5,v1.begin()); copy(t2,t2+5,v1.begin()+5); inplace_merge(v1.begin(), v1.begin()+5,v1.end()); for_each(v1.begin(), v1.end(), Out(cout));cout<<endl; return 0;}Program outputs:**A.** 1 2 3 4 5 6 10 8 7 9**B.** compilation error **C.** 1 2 3 4 5 6 7 8 9 10**D.** 3 2 4 1 5 6 10 8 7 9**E.** 3 2 4 1 5 6 7 8 9 10**Answer: C**

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