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**NEW QUESTION: 1**Which component shifts processing of the communications protocol stack (TCP/IP) from the server processor to the Network Interface Card (NIC), lowering CPU overhead and use?**A.** GBIC**B.** TCP Offload Engine**C.** software iSCSI initiator**D.** network switch**Answer: B**

**NEW QUESTION: 2**Refer to the exhibit.Given the output shown from this Cisco Catalyst 2950, what is the reason that interface FastEthernet 0/10 is not the root port for VLAN 2?**A.** This switch interface has a higher path cost to the root bridge than another in the topology.**B.** This switch is running RSTP while the elected designated switch is running 802.1d Spanning Tree.**C.** This switch has more than one interface connected to the root network segment in VLAN 2.**D.** This switch has a lower bridge ID for VLAN 2 than the elected designated switch.**Answer: A**  
Explanation:Since the port is in the blocked status, we must assume that there is a shorter path to the root bridge elsewhere.

**NEW QUESTION: 3**A media company has more than 100TB of data to be stored and retrieved infrequently. However, the company occasionally receives requests for data within an hour. The company needs a low-cost retrieval method to handle the requests. Which service meets this requirement?**A.** Amazon S3 Standard**B.** Amazon Glacier standard retrievals**C.** Amazon Glacier bulk retrievals**D.** Amazon S3 Standard Infrequent Access**Answer: D**

**NEW QUESTION: 4**Echo is always present to some extent in all voice networks. For echo to be a problem, which three conditions must exist? (Choose three.)**A.** an analog two-wire to four-wire hybrid operating below 600 ohm impedance**B.** sufficient power from the talker's side to cause listener echo**C.** sufficient delay in echo return for echo to be perceived as annoying**D.** sufficient echo amplitude to be perceived as annoying**E.** analog leakage between analog Tx and Rx paths**Answer: C,D,E**Explanation:Echo is perceived as annoying when all of these conditions are true: Signal leakage between the analog Tx and Rx paths. Sufficient delay in echo return. Sufficient echo amplitude.

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