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NEW QUESTION: 1 Which method is used by Hitachi Unified Storage (HUS) systems to protect against loss of data in cache should a total power failure occur? **A.** The battery power de-stages data in cache to an on-board SSD drive. **B.** The HUS mirrors all cache writes to on-board SSD drives. **C.** The battery power de-stages data in cache to on-board Flash memory. **D.** The HUS uses non-volatile cache so data is always protected. **Answer: C**

NEW QUESTION: 2 Which of the following are considered Bluetooth security violations? Each correct answer represents a complete solution. Choose two. **A.** SQL injection attack **B.** Bluebug attack **C.** Cross site scripting attack **D.** Social engineering **E.** Bluesnarfing **Answer: B,E**

NEW QUESTION: 3 In Cisco IOS routers, which chipset is the PVDM2-32 DSP hardware based on? **A.** C5510 **B.** Broadcom 1500 **C.** C5441 **D.** C5421 **E.** C549 **Answer: A** Explanation: Table 6-2 DSP Resources on Cisco IOS Hardware Platforms with C5510 Chipset
Maximum Number of Voice Terminations (Calls) per DSP and per Module
Hardware Medium Flex Mode 1 Module or DSP Complexity High Complexity (240 MIPS)
Chassis Configuration (8 calls per DSP) (6 calls per DSP) per DSP) VG-224 Fixed at 4 DSPs N/A 24 calls per N/A platform Supported codecs: * G.711 (a-law, mu-law) * G.729a NM-HD-1V2 Fixed at 1 DSP 4 calls per NM 4 calls per NM 240 MIPS per NM NM-HD-2V Fixed at 1 DSP 8 calls per NM 6 calls per NM 240 MIPS per NM NM-HD-2VE Fixed at 3 DSPs 24 calls per NM 18 calls per NM 720 MIPS per NM NM-HDV2 1 to 4 of: Calls per PVDM: Calls per PVDM: MIPS per NM-HDV2- PVDM2-83(1/2 4 3 PVDM: 2T1/E1 DSP) 8 6 120 NM-HDV2- PVDM2-16 (1 16 12 240 1T1/E1 DSP) 24 18 480 PVDM2-32 (2 32 24 720 DSPs) 960 PVDM2-48 (3 DSPs) PVDM2-64 (4 DSPs) Reference: http://www.cisco.com/en/US/docs/voice_ip_comm/cucm/srnd/4x/42media.html

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