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NEW QUESTION: 1Which component shifts processing of the communications protocol stack (TCP/IP) from the server processor to the Network Interface Card (NIC), lowering CPU overhead and use?**A.** TCP Offload Engine**B.** network switch**C.** GBIC**D.** software iSCSI initiator**Answer: A**

NEW QUESTION: 2Refer to the exhibit.Given the output shown from this Cisco Catalyst 2950, what is the reason that interface FastEthernet 0/10 is not the root port for VLAN 2?**A.** This switch interface has a higher path cost to the root bridge than another in the topology.**B.** This switch is running RSTP while the elected designated switch is running 802.1d Spanning Tree.**C.** This switch has a lower bridge ID for VLAN 2 than the elected designated switch.**D.** This switch has more than one interface connected to the root network segment in VLAN 2.**Answer: A**
Explanation:Since the port is in the blocked status, we must assume that there is a shorter path to the root bridge elsewhere.

NEW QUESTION: 3A media company has more than 100TB of data to be stored and retrieved infrequently. However, the company occasionally receives requests for data within an hour. The company needs a low-cost retrieval method to handle the requests. Which service meets this requirement?**A.** Amazon S3 Standard**B.** Amazon Glacier bulk retrievals**C.** Amazon S3 Standard Infrequent Access**D.** Amazon Glacier standard retrievals**Answer: C**

NEW QUESTION: 4Echo is always present to some extent in all voice networks. For echo to be a problem, which three conditions must exist? (Choose three.)**A.** sufficient echo amplitude to be perceived as annoying**B.** sufficient power from the talker's side to cause listener echo**C.** sufficient delay in echo return for echo to be perceived as annoying**D.** analog leakage between analog Tx and Rx paths**E.** an analog two-wire to four-wire hybrid operating below 600 ohm impedance**Answer: A,C,D**Explanation:Echo is perceived as annoying when all of these conditions are true: Signal leakage between the analog Tx and Rx paths. Sufficient delay in echo

return. Sufficientecho amplitude.

http://www.cisco.com/en/US/tech/tk652/tk698/technologies_tech_note09186a0080149a1f.s---html

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