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NEW QUESTION: 1Which statements explain the difference between the Itanium 2 9M and the mx2 processors? (Select two.)**A.** tanium 2 9M supports two processors on one socket.**B.** tanium mx2 is the upgrade path from Itanium 2 9M.**C.** tanium mx2 supports two processors on one socket**D.** tanium 2 9M offers more Level 3 cach**E.** tanium mx2 offers more Level 3 cach**E.**

Answer: C,D

NEW QUESTION: 2In the course of performing a risk analysis, an IS auditor has identified threats and potential impacts. Next, the IS auditor should:**A.** identify information assets and the underlying systems.**B.** identify and evaluate the existing controls.**C.** disclose the threats and impacts to management.**D.** identify and assess the risk assessment process used by management.**Answer: B****Explanation:**It is important for an IS auditor to identify and evaluate the existing controls and security once the potential threats and possible impacts are identified. Upon completion of an audit an IS auditor should describe and discuss with management the threats and potential impacts on the assets.

NEW QUESTION: 3Which statement about combinations of inputs and preconditions is true for a large system?**A.** It is not possible to test any of them**B.** It is easy to test them all in a short time **C.** It is essential to test them all in order to do good testing**D.** It is not practically possible to test them all**Answer: D**

NEW QUESTION: 4When preparing a desktop for use in a virtual desktop infrastructure, why is it important to optimize the desktop image by disabling the recommended services?**A.** The Microsoft Windows defaults will require a time-intensive Microsoft Sysprep, disabling the VMware QuickPrep functionality.**B.** The Microsoft Windows defaults will cause intermittent PCoIP session interrupts or connection issues.**C.** The Microsoft Windows default will cause excessive IOPS and can unnecessarily expand the desktop'sVMDK files.**D.** The Microsoft Windows defaults will cause desktops to enter theAgent Unreachableerror state.**Answer: C**

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