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NEW QUESTION: 1What is Project Portfolio Management?**A.** Management of all projects undertaken by a company.**B.** Management of a project by utilizing a portfolio of general management skills, such as planning, organizing, staffing, executing and controlling.**C.** Management of a project by dividing the project into more manageable sub- projects.**D.** Management of a collection of projects that are grouped together to facilitate effective management and meet strategic business objectives.**Answer: D**

NEW QUESTION: 2Which of the following is a valid ESD safety precaution?**A.** Do not wear polyester clothing while working on ESD sensitive components**B.** You should place ESD-sensitive circuits on top of metal work surfaces.**C.** Keep ion generators away from circuit board or assembly containing ESD-sensitive circuits.**D.** Always handle logic board by grasping the heat sinks.**Answer: A**Explanation:Explanation/Reference:Reference:
<http://www.peachpit.com/articles/article.aspx?p=760956> (see ESD safety guidelines)

NEW QUESTION: 3Your customer has a dual socket server board; and they have complained to you that their customer is constantly getting PCIe errors. At your request they send you the System Event log. On investigation of the SEL by you, you discover some events that look like the following: TimeStamp:03/05/2014 12:18:41 SensorName:PCIe Cor Sensor. Bus:0, device:3, function:2Which one of the following represents the correct order in which troubleshooting should occur?**A.** Force slower PCIe link speeds in the BIOS setup utility.**B.** B, C, F**C.** A, B, E**D.** If the error happens regularly, try to cross-test PCIe devices to see if the error follows the card or stays with the slot.**E.** List through the error codes from the User guide to find the translation table for the SEL in order to identify the failing device and link speed.**F.** If error happens very rarely, disable AER reporting in BIOS**G.** A, F, E**H.** Check SEL troubleshooting guide in order to understand PCIe bus, device, function of the card that generates the error. Then check TPS to identify the slot to find the suspect component.**I.** Rerun the FRU/SDR to force the system to detect the correct PCIe generation for this device.**J.** A, D, C**Answer: F**

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